

The diagram illustrates the Utopia Bus architecture, showing two modes of operation: STANDBY and ACTIVE.

**STANDBY Device:**

- Inputs:** Utopia Port n, TXClav<sub>n</sub> (High-Z), TXClav<sub>0</sub> (High-Z).
- Internal Logic:** Utopia Out Ctrl, TX Out Ctrl, TDM Port n, TDM Port 0.
- Outputs:** DSTo<sub>n</sub> (High-Z), TXCK<sub>n</sub> (High-Z), TXSYNC<sub>n</sub> (High-Z), DSTo<sub>0</sub> (High-Z), TXCK<sub>0</sub> (High-Z), TXSYNC<sub>0</sub> (High-Z).
- Control:** Ctrl = Standby.

**ACTIVE Device:**

- Inputs:** Utopia Port n, TXClav<sub>n</sub> (Normal\*), TXClav<sub>0</sub> (Normal\*).
- Internal Logic:** Utopia Out Ctrl, TX Out Ctrl, TDM Port n, TDM Port 0.
- Outputs:** DSTo<sub>n</sub> (Normal\*), TXCK<sub>n</sub> (Normal\*), TXSYNC<sub>n</sub> (Normal\*), DSTo<sub>0</sub> (Normal\*), TXCK<sub>0</sub> (Normal\*), TXSYNC<sub>0</sub> (Normal\*).
- Control:** Ctrl = Active.

**Utopia Bus:**

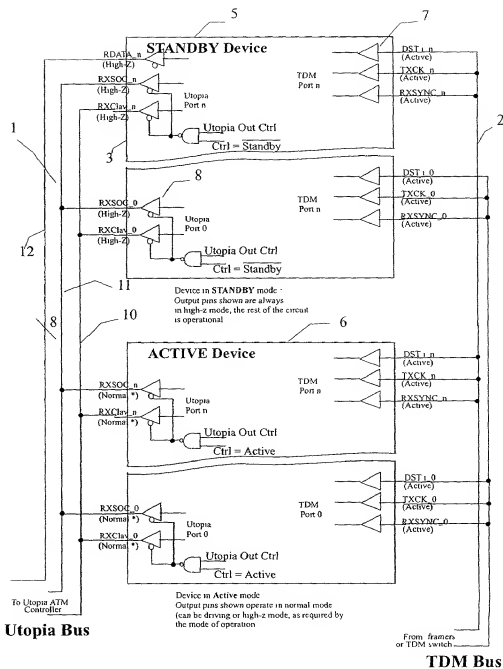
- Utopia ATM Controller:** Connected to Utopia Port n and Utopia Port 0.
- To frames or TDM switch:** Connected to DSTo<sub>n</sub>, TXCK<sub>n</sub>, TXSYNC<sub>n</sub>, DSTo<sub>0</sub>, TXCK<sub>0</sub>, TXSYNC<sub>0</sub>.

**Device in ACTIVE mode:**

Output pins shown operate in normal mode (can be driving or in high-z mode as required by the mode of operation)

### ATM Controller to MPHY Devices

Fig. 1



### MPHY Devices to ATM Controller

Fig. 2